

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	6080	("electroless plating" and ((tft "thin film transistor") and (lcd "liquid crystal display")))) and etch\$	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/07 09:00
L3	88403	(solution and reductant and metal)	US-PGPUB; USPAT; USOCR	OR	ON	2006/09/11 16:06
L4	4307287	photoresist and etch\$3 and remov\$3	US-PGPUB; USPAT; USOCR	OR	ON	2006/09/11 15:56
L5	104	first and second	US-PGPUB; USPAT; USOCR	OR	ON	2006/09/11 16:07
L6	55	(2 and 3 and 4)	US-PGPUB; USPAT; USOCR	OR	ON	2006/09/11 16:08
L7	50	(2 and 3) and first and second.clm.	US-PGPUB; USPAT; USOCR	OR	ON	2006/09/11 16:04
L8	7441	(2 and 3) and (first and second).clm.	US-PGPUB; USPAT; USOCR	OR	ON	2006/09/11 16:05
L9	1513792	(solution and reductant)	US-PGPUB; USPAT; USOCR	OR	ON	2006/09/11 16:06
L10	104	first and second and metal	US-PGPUB; USPAT; USOCR	OR	ON	2006/09/11 16:07
S1	226703	3 and 8 and 9	US-PGPUB; USPAT; USOCR	OR	ON	2006/09/11 16:08
S2	22	"438"/\$.ccls. or "257"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/11 15:52
S3	57	((((tft "thin film transistor") and (lcd "liquid crystal display")) and ("438"/\$.ccls. or "257"/\$.ccls.)) and "electroless plating"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/06 16:49
		"electroless plating" and ((tft "thin film transistor") and (lcd "liquid crystal display"))	USPAT	OR	OFF	2001/12/06 16:54

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S4	68	"electroless plating" and ((tft "thin film transistor") and (lcd "liquid crystal display"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2001/12/07 11:31
S5	68	"electroless plating" and ((tft "thin film transistor") and (lcd "liquid crystal display"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/25 14:38
S6	5830	reductant	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2001/12/07 11:35
S7	12	"mixed solution" same reductant	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/07 11:36
S8	6664	reductant	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/07 11:36
S9	17	"mixed solution" same reductant	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/07 11:36
S10	101	reductant and "electroless plating"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/07 11:58
S11	488	438/158	USPAT	OR	OFF	2001/12/10 11:24
S12	297	438/589	USPAT	OR	OFF	2001/12/07 15:32
S13	1471	reductant with metal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/10 07:36
S14	20321	(tft "thin film transistor") and (lcd "liquid crystal display")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/10 07:39

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S15	1	(reductant with metal) and ((tft "thin film transistor") and (lcd "liquid crystal display"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/10 07:40
S16	109	reductant and "electroless plating"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/10 08:10
S17	271	438/678	USPAT	OR	OFF	2001/12/10 11:26
S18	150	438/678 and electroless	USPAT	OR	OFF	2001/12/10 11:31
S19	150	438/678 and electroless	USPAT	OR	ON	2001/12/10 13:34
S20	0	438/6687	USPAT	OR	ON	2001/12/10 13:35
S21	551	438/687	USPAT	OR	ON	2003/02/20 09:06
S22	200403	"sulfuric acid" h2so4 "cupric sulfate" "cuso4.5h2o" "cuso4*5h2o"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/11 08:43
S23	1031	("sulfuric acid" h2so4) and ("cupric sulfate" "cuso4.5h2o" "cuso4*5h2o")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/11 08:43
S24	69	((("sulfuric acid" h2so4) and ("cupric sulfate" "cuso4.5h2o" "cuso4*5h2o"))) and "electroless plating"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/11 08:45
S25	52	((("sulfuric acid" h2so4) and ("cupric sulfate" "cuso4.5h2o" "cuso4*5h2o"))) and "electroless plating") and (hcho formaldehyde hydrazine "sodium phosphate" nah2po2 "sodium borate" nabh4 dmab "dimethyl amine boran")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/11 08:48
S26	53	((("sulfuric acid" h2so4) and ("cupric sulfate" "cuso4.5h2o" "cuso4*5h2o"))) and "electroless plating") and (hcho formaldehyde hydrazine "sodium phosphate" nah2po2 "sodium borate" nabh4 dmab "dimethyl amine borane")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/11 08:50

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S27	31	((("sulfuric acid" h2so4) and ("cupric sulfate" "cuso4.5h2o" "cuso4*5h2o")) and "electroless plating") and (hcho formaldehyde hydrazine "sodium phosphate" nah2po2 "sodium borate" nabh4 dmab "dimethyl amine borane")) and (electrode tft "thin film transistor")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/11 10:45
S28	2	"6284652".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 11:17
S29	169930	"gold chloride" aucl2 "sodium chloride" nacl	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/11 10:48
S30	140	("gold chloride" aucl2) and ("sodium chloride" nacl)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/11 10:49
S31	202501	formaldehyde hcho hydrazine "sodium phosphate" nah2po2 "sodium borate" nabh4 "dimethyl amine borane" dmab	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/11 10:51
S32	65	((("gold chloride" aucl2) and ("sodium chloride" nacl)) and (formaldehyde hcho hydrazine "sodium phosphate" nah2po2 "sodium borate" nabh4 "dimethyl amine borane" dmab)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/11 10:56
S33	38713	TFT "thin film transistor"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/11 10:53
S34	194695	lcd "liquid crystal display"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/11 10:54

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S35	0	((("gold chloride" aucl2) and ("sodium chloride" nacl)) and (formaldehyde hcho hydrazine "sodium phosphate" nah2po2 "sodium borate" nabh4 "dimethyl amine borane" dmab)) and (TFT "thin film transistor"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/11 10:54
S36	17029	"electroless plating"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/11 10:55
S37	2	((("gold chloride" aucl2) and ("sodium chloride" nacl)) and (formaldehyde hcho hydrazine "sodium phosphate" nah2po2 "sodium borate" nabh4 "dimethyl amine borane" dmab)) and "electroless plating"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2001/12/11 10:56
S38	1	"5807607".pn.	USPAT	OR	ON	2003/02/20 09:21
S39	804	glass with aerogel	USPAT	OR	ON	2003/02/20 09:21
S40	4529	((tft "thin film transistor") and (lcd "liquid crystal display")) and ("438"/\$.ccls. or "257"/\$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/17 14:13
S41	133	gate with (trench or recess) with glass	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/17 14:14
S42	358072	gate	US-PGPUB; USPAT	OR	ON	2003/06/20 11:42
S43	747401	trench recess groove	US-PGPUB; USPAT	OR	ON	2003/06/20 11:42
S44	584371	glass	US-PGPUB; USPAT	OR	ON	2003/06/20 11:42
S45	16334	tft	US-PGPUB; USPAT	OR	ON	2003/06/20 11:42
S46	55387	lcd	US-PGPUB; USPAT	OR	ON	2003/06/20 11:43
S47	7	((((tft "thin film transistor") and (lcd "liquid crystal display")) and ("438"/\$.ccls. or "257"/\$.ccls.)) and (gate with (trench or recess) with glass)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 09:11
S48	119	gate with (trench recess groove) with glass	US-PGPUB; USPAT	OR	ON	2003/06/20 11:43

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S49	10	(gate with (trench recess groove) with glass) and tft and lcd	US-PGPUB; USPAT	OR	ON	2003/06/20 11:44
S50	54	(glass with aerogel) with dielectric	USPAT	OR	ON	2003/06/20 16:09
S51	4382	lcd with glass with substrate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/25 14:39
S52	41	(lcd with glass with substrate) with (trench groove recess)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/25 14:40
S53	2253	"trench gate"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 09:13
S54	5169213	@ad>"19991110" @rlad>"19991110"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 09:14
S55	984	"trench gate" not (@ad>"19991110" @rlad>"19991110")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 09:54
S56	12	"trench gate" with glass	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 09:53
S57	7	("trench gate" with glass) not (@ad>"19991110" @rlad>"19991110")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 09:45
S58	595	"single layer substrate"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 10:01
S59	0	"trench gate" same "single layer substrate"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 09:54

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S60	0	"trench gate" and "single layer substrate"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 09:54
S61	5	"single layer substrate" same gate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 09:56
S62	2	"single layer substrate" same trench	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 09:56
S63	271	"single layer substrate" not (@ad>"19991110" @rlad>"19991110")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 09:58
S64	14975	"single substrate"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 10:01
S65	7	"trench gate" same "single substrate"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 10:01
S66	2	"5705230".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/12 11:17
S67	3537	"lift off method"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 08:37
S68	3107	"lift-off method"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 08:37
S69	20364	"LCD device"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 08:40

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S70	14	S67 and S69	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 08:38
S71	14	S67 and S69	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 08:38
S72	20364	"LCD device"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 08:40
S73	14	S67 and S72	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 08:40